//Configure Pins

//“Write 0x2 to **conf\_spi0\_cs0** offset 0x95C to enable (**SCL**) for MODE2 w/o pullup”

//“Write 0x2 to **conf\_spi0\_d1** offset 0x958 to enable (SDA) for MODE2 w/o pullup”

//“Write 0x2 to **CM\_PER\_I2C1\_CLKCTRL** offset 0x48 to enable I2C1 Clock.”

//“Write 0x03 to **I2C\_PSC** (Clock Prescalar Register) offset 0xB0 for ICLK of 12 MHz”

//"Write 0x35 to **I2C\_SCLL** (SCL Low Time Register) offset 0xB4 for **tLOW** to get 100kbps (5us-Low)"

//"Write 0x37 to **I2C\_SCLH** (SCL High Time Register) offset 0xB8 for **tHIGH** to get 100kbps (5us-High)"

// “Write 0x001 to **I2C\_OA** (Own Address Register) offset 0xA8 to configure Own Address”

// “Write 0x8000 to **I2C\_CON** (Configuration Register) offset 0xA4 to take out of reset, enable I2C1 module”

//"Read-Modify-Write 0x600 to **I2C\_CON** (Configuration Register)offset 0xA4 to configure mode."

//"Write 0x0000 to **I2C\_IRQENABLE\_SET** (Interrupt Enable Set Register) offset 0x2C to enable Polling”

//"Write 0x0000 to **I2C\_BUF** (Buffer Configuration Register) offset 0x94 to set Transmit and Receive .”

//"Write 0x78 to **I2C\_SA** (Slave Address Register) offset 0xAC Slave address value"

//"Write 0x9 to **I2C\_CNT** (Data Count Register) offset 0x98 to set number of transmission Bytes"

//"Read mask 0x00001000 from **I2C\_IRQSTATUS\_RAW** (I2C Status Raw Register) offset 0x24 to check bus status.

//"Read-Modify-Write 0x00000003 to **I2C\_CON** (Configuration Register) offset 0xA4 to queue Start/Stop Cond.”

// “Read mask 0x00000008 **I2C\_IRQSTATUS\_RAW** (I2C Status Raw Register) offset 0x24 see if data is ready.”

//"If **RRDY** is "1", data is ready for read. Read **I2C\_DATA** (Data Access Register) offset 0x9C."

// “If "1", Write 0x00000008 to **I2C\_IRQSTATUS** (Status Register) offset 0x28 to Clear **RRDY”**

// “Read mask 0x00000010 from **I2C\_IRQSTATUS\_RAW** (I2C Status Raw Register) offset 0x24 to see if write ready”

//"If **XRDY** is "1" FIFO is ready for data. Write to **I2C\_DATA** (Data Access Register) offset 0x9C .”

// “If "1", Write 0x00000010 to **I2C\_IRQSTATUS** (Status Register) offset 0x28 to clear **XRDY.”**